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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Mikio Tanaka, et al.

Examiner: Matthew C. Dooley

Serial No.: 09/819,860

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Filed: March 28, 2001

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For: MEMORY LSI FAILURE
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ANALYSIS METHOD THEREOF

Dated: May 25, 2004

Confirmation No. 3671

Commissioner for Patents
P.O. Box 1450,
Alexandria, VA 22313-1450

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Technology Center 2100

AMENDMENT UNDER 37 C.F.R. § 1.111

Sir:

In response to the Office Action dated February 27, 2004, under the provisions of 37 C.F.R. § 1.111, Applicant respectfully requests that the above-identified Application be amended as follows and that the Application be reconsidered and allowed.

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 25, 2004.

Dated: May 25, 2004


George Brieger